File   View   Edit   Tools   Window   Help

Active
- L2: (2) (DC-AC near2 (driver or converter or switch'
- L7: (0) (multiplexed same (high adj voltage) same DC
- L6: (27) (DC-AC near2 (driver or converter or switcl
- L9: (5588) AC and DC and (converter or inverter) ar
- L10: (126) 9 and (master near2 (clock adj signal))
- L11: (116) 10 and logic
- L12: (204) (((DC near2 AC) or (DC-AC)) near3 (dr
- L13: (173) 12 and logic
- L14: (204) (((DC near2 AC) or (DC-AC) or (DC/A(
- L15: (264) (((DC near2 AC) or (DC-AC) or (DC/A(
- L16: (226) 15 and logic
- L17: (35) 16 and ('315'/S.ccls. or '323'/S.ccls. or '3
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

DBs   US-PGPUB, USPAT, EPO, JPO, DERWENT
Default operator: OR

☑ Plurals
☑ Highlight all hit terms substr

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Er	Erro
1	BRS	L2	2	(DC-AC near2 (driver or converter or switch$2)) and DC and ((high and low) near2 side) and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:15			
2	BRS	L7	0	(multiplexed same (high adj voltage) same DC-AC same (driver or converter or switch$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:14			
3	BRS	L6	27	(DC-AC near2 (driver or converter or switch$2)) and DC and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:35			
4	BRS	L9	5588	AC and DC and (converter or inverter) and switch$3 and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:29			
5	BRS	L10	126	9 and (master near2 (clock adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:31			
6	BRS	L11	116	10 and logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:31			
7	BRS	L12	204	(((DC near2 AC) or (DC-AC)) near3 (driver or converter or inverter or switch$2)) and decoder and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 09:59			
8	BRS	L13	173	12 and logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 10:01			
9	BRS	L14	204	(((DC near2 AC) or (DC-AC) or (DC/AC)) near3 (driver or converter or inverter or switch$2)) and decoder and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 10:01			
10	BRS	L15	264	(((DC near2 AC) or (DC-AC) or (DC/AC)) near3 (driver or converter or inverter or switch$2)) and decod$3 and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 10:01			
11	BRS	L16	226	15 and logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 10:01			
12	BRS	L17	35	16 and ('315'/S.ccls. or '323'/S.ccls. or '363'/S.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/02/18 10:02			

Ready